Computer Architectures for Spatially Distributed Data

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Computer Architectures For Spatially Distributed Data:

Computer Architectures for Spatially Distributed Data (Volume 18). H Freeman (ed),1985 *Computer Architectures for Spatially Distributed Data* Herbert Freeman,G.G. Pieroni,2013-11-09 These are the proceedings of a NATO Advanced Study Institute ASI held in Cetraro Italy during 6 17 June 1983 The title of the ASI was Computer Arehiteetures for SpatiaZZy vistributed Vata and it brouqht together some 60 participants from Europe and America Presented ere are 21 of the lectures that were delivered The articles cover a wide spectrum of topics related to computer architecture s specially oriented toward the fast processing of spatial data and represent an excellent review of the state of the art of this topic For more than 20 years now researchers in pattern recognition image processing meteorology remote sensing and computer engineering have been looking toward new forms of computer architectures to speed the processing of data from two and three dimensional processes The work can be said to have commenced with the landmark article by Steve Unger in 1958 and it received a strong forward push with the development of the ILIAC III and IV computers at the University of Illinois during the 1960 s One clear obstacle faced by the computer designers in those days was the limitation of the state of the art of hardware when the only switching devices available to them were discrete transistors As aresult parallel processing was generally considered to be imprae tieal and relatively little progress was made *Mapping and Spatial Modelling for Navigation* Louis F. Pau,2012-12-06 The successful implementation of applications in spatial reasoning requires paying attention to the representation of spatial data In particular an integrated and uniform treatment of different spatial features is necessary in order to enable the reasoning to proceed quickly Currently the most prevalent features are points rectangles lines regions surfaces and volumes As an example of a reasoning task consider a query of the form find all cities with population in excess of 5 000 in wheat growing regions within 10 miles of the Mississippi River Note that this query is quite complex It requires processing a line map for the river creating a corridor or buffer to find the area within 10 miles of the river a region map for the wheat and a point map for the cities Spatial reasoning is eased by spatially sorting the data i e a spatial index In this paper we show how hierarchical data structures can be used to facilitate this process They are based on the principle of recursive decomposition similar to divide and conquer methods In essence they are used primarily as devices to sort data of more than one dimension and different spatial types The term quadtree is often used to describe this class of data structures In this paper we focus on recent developments in the use of quadtree methods We concentrate primarily on region data For a more extensive treatment of this subject see SameS4a SameSSa SameSSb SameSSc SameSga SameSgbj □□□□□□□□□□□□□□□ □□□□□□□□ □□□□□□□ (Japan),1990 □□□□□□□□□□□□□□□□□□□□□□□ ,1985 *Parallel Computer Architecture* David Culler,Jaswinder Pal Singh,Anoop Gupta,1999 This book outlines a set of issues that are critical to all of parallel architecture communication latency communication bandwidth and coordination of cooperative work across modern designs It describes the set of techniques available in hardware and in software to address each issues and explore how the various techniques

interact **IEEE Computer Society Workshop on Computer Architecture for Pattern Analysis and Image Database Management** ,1983 **Bibliographic Guide to Conference Publications** New York Public Library. Research Libraries,1987 Vols for 1975 include publications cataloged by the Research Libraries of the New York Public Library with additional entries from the Library of Congress MARC tapes International Conference on Data Engineering International Conference on Data Engineering,1984 **Proceedings of the International Conference on Application Specific Array Processors** Sun Yuan Kung,1990 Proceedings of the Sixth Annual Conference and Exposition, Dallas Convention Center, Dallas, Texas, April 14-18, 1985: Technical sessions National Computer Graphics Association (U.S.). Conference,National Computer Graphics Association (U.S.). Conference and Exposition,1985 **1983 IEEE Computer Society Workshop on Computer Architecture for Pattern Analysis and Image Database Management** ,1983 **Programming Models for Massively Parallel Computers, 1993** Wolfgang Giloi,Stefan Jähnichen,Bruce D. Shriver,1993 Proceedings Parallel Computing **Computers in earth sciences for natural resources characterization** J. J. Royer,1984 **International Conference on Data Engineering** ,1984 *Proceedings of the ... Midwest Symposium on Circuits and Systems* ,1988 Memory Architecture & Parallel Access Michael Gössel,Burghard Rebel,Reiner Creutzburg,1994 This text covers the subject of memory systems that have the possibility of parallel access to windows consisting of several memory words each The authors present the most significant theoretical foundations currently existing *International Books in Print* ,1997 **Computer Book Review** ,1986 *Government Reports Announcements & Index* ,1988-11

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