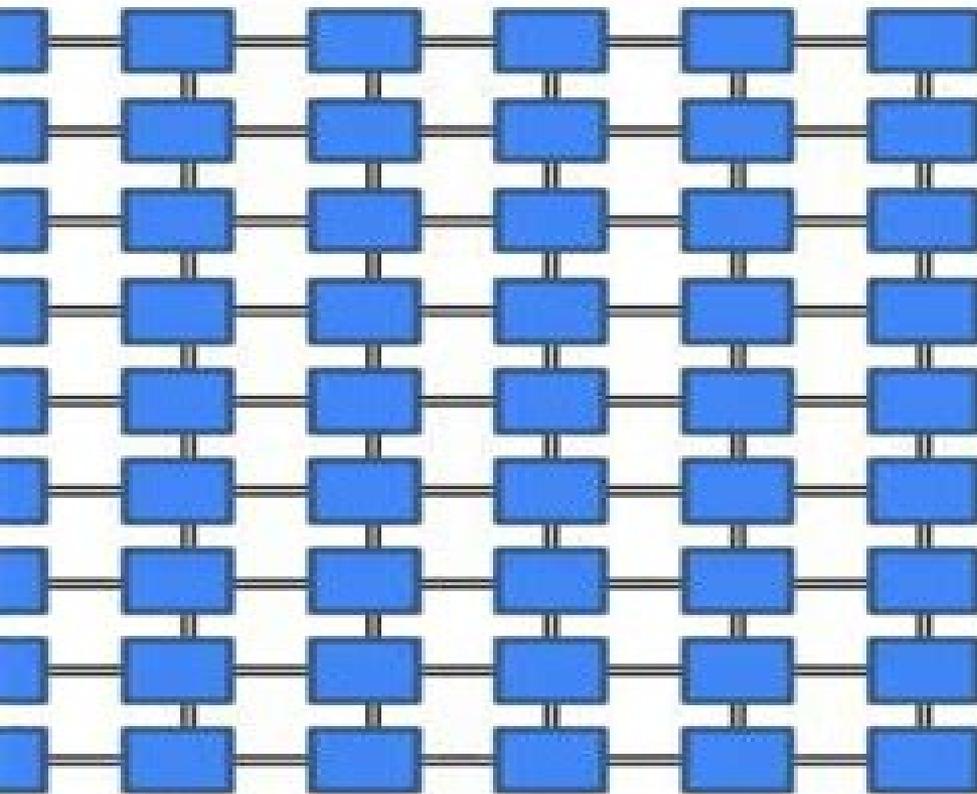


Field Programmable Gate Array



Architecture

Digital logic - ALU

Multipliers - DSP

Large Local memory

Lookup tables - LUT

Built in processors

Parallel Operation

Massive 2D Array

Impressive order

Extreme inter-conn

er millions of gates, 1000s of ALUs / Integer / Floating-
bits of local memory / look-up tables and multi-gigabi

Digital Design Using Field Programmable Gate Arrays

Didier Musso



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